

# TRANSMITTAL FORM



Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: SPLX.P0060

## Diagonal Wiring Architecture for Integrated Circuits

First Named Inventor: Mr. Steven Teig

### SUBMITTED BY

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### Attached Files:

bibd-transmittal	EFSapds.xml
specification	P0004EFS.xml
fee-transmittal	EFSfee.xml

Comments:

09681776-060301

# FEE TRANSMITTAL

Electronic Version 1.0.4

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*Patent fees are subject to annual revisions on or about October 1st of each year.*

Large Entity

**TOTAL FEES AUTHORIZED: \$ 1516**

## BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 710

Subtotal For Basic Filing Fee: \$ 710

## EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 47	103	\$ 18	27	\$ 486
Independent Claims: 7	102	\$ 80	4	\$ 320

Subtotal For Extra Claims Fees: \$ 806

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Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
USPTO Use Only

EFS ID: 11575  
Application ID: 09681776  
Title of Invention: Diagonal Wiring Architecture for Integrated Circuits  
First Named Inventor: Steven Teig  
Domestic/Foreign Application: Domestic Application  
Filing Date: null  
Effective Receipt Date: 2001-06-03  
Submission Type: Utility Patent Filing  
Filing Type: null  
Confirmation Number: 0  
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